				<u></u>		
L	Hits	Search Text	DB	Time stamp		
Number						
1	2485	(memory storage) near3 (module bank sector) and ((cache storage memory) with tag)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/04 16:27		
2	371	((storage cache memory) adj4 (controller logic)) and way and ((evict\$3 victim) near3 (line entry buffer queue register))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/04 16:26		
3	120	((memory storage) near3 (module bank sector) and ((cache storage memory) with tag)) and (((storage cache memory) adj4 (controller logic)) and way and ((evict\$3 victim) near3 (line entry buffer queue register)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003 <u>)</u> 09/04 16:29 }		
4	4106	711/\$.ccls. and (((memory storage) near3 (module bank sector) and ((cache storage memory) with tag)) and (((storage cache memory) adj4 (controller logic)) and way and ((evict\$3 victim) near3 (line entry buffer queue register)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/04 16:29		
5	946	(memory storage) near3 (module bank sector) and ((cache storage memory) with tag) and ((stor\$3 writ\$3) adj3 back)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/04 16:28		
6	102	(((storage cache memory) adj4 (controller logic)) and way and ((evict\$3 victim) near3 (line entry buffer queue register))) and ((memory storage) near3 (module bank sector) and ((cache storage memory) with tag) and ((stor\$3 writ\$3) adj3 back))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/04 16:29		

L	Hits	Search Text	DB	Time stamp
Number		<u> </u>	USPAT;	2003/09/04
8	7366	((memory cache storage) adj4 (controller		16:53
		logic circuit)) same (cache memory	US-PGPUB;	16:53
		storage) near4 (index tag field)	EPO; JPO;	
			DERWENT	
9	144	((memory storage) near3 (bank module	USPAT;	2003/09/04
		sector block)) and ((victim eviction) near4	US-PGPUB;	17:05
		(line entry cache buffer queue register)) and	EPO; JPO;	
		((memory storage) near3 (module bank	DERWENT	•
		sector) and ((cache storage memory) with		<u>}</u>
		tag))		``
10	33	(decoder deserializer) and (((memory	USPAT;	2003/09/04
		storage) near3 (bank module sector block))	US-PGPUB;	17:05
		and ((victim eviction) near4 (line entry	EPO; JPO;	
		cache buffer queue register)) and ((memory	DERWENT	
	.∀.	storage) near3 (module bank sector) and		
	9	((cache storage memory) with tag)))	ĺ	
11	452	(sequencer serializer) and (((memory cache	USPAT;	2003/09/04
111	. 402	storage) adj4 (controller logic circuit))	US-PGPUB;	17:04
		same (cache memory storage) near4 (index	EPO; JPO;	
		tag field))	DERWENT	
12	322	(decoder deserializer) and ((sequencer	USPAT;	2003/09/04
12	322	serializer) and (((memory cache storage)	US-PGPUB;	17:05
			EPO; JPO;	
		adj4 (controller logic circuit)) same (cache	DERWENT	·
		memory storage) near4 (index tag field)))	USPAT:	2003/09/04
13	374	((memory storage) near3 (bank module	1	17:06
		sector block)) and ((sequencer serializer)	US-PGPUB;	17:06
		and (((memory cache storage) adj4	EPO; JPO;	
		(controller logic circuit)) same (cache	DERWENT	
		memory storage) near4 (index tag field)))	:_	
14	255	((memory storage) near3 (bank module	USPAT;	2003/09/04
		sector block)) and ((decoder deserializer)	US-PGPUB;	17:06
		and ((sequencer serializer) and (((memory	EPO; JPO;	
		cache storage) adj4 (controller logic	DERWENT	
		circuit)) same (cache memory storage)		
		near4 (index tag field))))		

L Number	Hits	Search Text	DB	Time stamp
1	49	(((system memory storage) adj3 (controller logic circuit)) with ((tag index field) adj3 (buffer array register))) same ((memory storage) adj3 (bank module sector block))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/04 19:54